



CCD Drivers

Device	# of Drivers	Max Operating Frequency (MHz)	Peak Output I _{PK} (A)	Rise Time (ns)	Fall Time (ns)	Turn-On Delay (ns)	Turn-Off Delay (ns)	I _S (mA)	R _{ON} (Ω)	Input Signal Range (V)	Input Supply Range (V _P)	Max Input Signal (V)	Output Signal Range (V)	Max Output Signal (V)	Package
EL7457	4	40	2	11	12	13	11.5	0.8	4.5	-V _P to +V _P	-4.5 to +16	16	-4.5 to +16	16	16 Ld QFN, 16 Ld QSOP, 16 Ld SOIC
ISL55110	2	100	3.5	1.5	1.5	40 (QFN Only)	40 (QFN Only)	3.5	3	0 to V _P	+2.7 to +5.5	5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP
ISL55111	2	100	3.5	1.5	1.5	40 (QFN Only)	40 (QFN Only)	3.5	3	0 to V _P	+2.7 to +5.5	5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP

Sensor Conditioners

Device	# of Devices/ Channels	# of Taps	Memory Type	Bus Interface Type	V _{CC} Range (V)	DCP Single Supply Range (V)	Resistance Taper	Wiper Current (mA)	Supply Current (μA)	Max Operating Temperature (°C)	End-to-End Tempo (ppm/°C)	Package
X96010	2	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to 5.5	Linear	±4	200	100	±100	14 Ld TSSOP
X96011	1	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to 5.5	Linear	±4	200	100	±100	14 Ld TSSOP
X96012	2	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to 5.5	Linear	±4	200	100	±100	14 Ld TSSOP

RF Power Amplifier Control

Device	26V (Typical) Instrumentation Anti-Dither (Drain Sense) Amplifier	IDQ Sense and Tracking	8-Bit (256-Tap) Gate Voltage Adjust Output	Gate Voltage Output Buffer	V _{REF} Input	Non-Volatile Storage of Gate Voltage Setting	Input for Temperature Compensation Circuits	Optional Automatic/ Periodic Control for Drain Current Sense to Gate Voltage Adjustment	Power-On Recall Circuits	Bus Interface	Package
X9470	Y	Y	Y	Y	Y	Y	Y	Y	Y	2-Wire	24 Ld TSSOP

Engine Knock Sensors

Device	Device Description	Package
HIP9011	Engine Knock Signal Processor	20 Ld SOIC

Multipliers

Device	BW (MHz)	SR (V/μs)	Key Spec.	I _S (mA)	V _{CC} Range (±V)	Package
HA-2556	57	450	Mult Error = 1.5%	18	5 to 15	16 Ld CerDIP, 16 Ld SOIC

Comparators

Device	# of Devices/ Channels	Response Time (ns)	I _S (mA)	Input Supply Range (V _P)	Max Input Signal (V)	Comparator Receiver Range (V)	Output Signal Range (V)	Max Output Signal (V)	I _{BIAS} (nA)	V _{OS} (max) (mV)	I _{OS} (nA)	Package
ISL55141	1	10	7	-18 to +18	18	-V _P to +V _P -5	-V _P to +V _P	18	10	50	10	14 Ld TSSOP, 16 Ld QFN
ISL55142	2	10	7	-18 to +18	18	-V _P to +V _P -5	-V _P to +V _P	18	10	50	10	20 Ld QFN, 20 Ld TSSOP
ISL55143	4	10	7	-18 to +18	18	-V _P to +V _P -5	-V _P to +V _P	18	10	50	10	36 Ld QFN